

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1	6525592.pn.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 13:56
2	BRS	L2	1	6525952.pn.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 14:09
3	BRS	L3	1	6317371.pn.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 14:29
4	BRS	L4	2	10/081375	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 14:30

	Type	L #	Hits	Search Text	DBs	Time Stamp
5	BRS	L5	2	4 and (concurrent\$4 or parallel)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:30
6	BRS	L6	1	2 and page with (cluster or sector)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:32
7	BRS	L7	254	(page or sector) with (double adj5 buffer\$4 or ping adj5 pong)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:33
8	BRS	L8	12	(page or sector) with (double adj5 buffer\$4 or ping adj5 pong).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 16:20

	Type	L #	Hits	Search Text	DBs	Time Stamp
9	BRS	L9	4	(page or sector) with (double adj5 buffer\$4 or ping adj5 pong).clm.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:39
10	BRS	L10	8	(gilbert and hyatt).in. and double adj5 buffer\$4	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 16:55
11	BRS	L11	4477	((input\$4 and output\$4) or (read\$4 and write\$4)) and double adj5 buffer\$4	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:46
12	BRS	L12	169	((input\$4 and output\$4) or (read\$4 and write\$4)) and double adj5 buffer\$4).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:48

	Type	L #	Hits	Search Text	DBs	Time Stamp
13	BRS	L13	1330	((input\$4 and output\$4) or (read\$4 and write\$4)) and double adj5 buffer\$4 and (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:49
14	BRS	L14	0	((input\$4 and output\$4) or (read\$4 and write\$4)) and double adj5 buffer\$4 and (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable)).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:50
15	BRS	L15	5	((input\$4 and output\$4) or (read\$4 and write\$4)) and double adj5 buffer\$4 and (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable)).clm.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:50
16	BRS	L16	1	((input\$4 and output\$4) or (read\$4 and write\$4)) same double adj5 buffer\$4 and (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable)).clm.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 15:51

	Type	L #	Hits	Search Text	DBs	Time Stamp
17	BRS	L17	0	((input\$4 and output\$4) or (read\$4 and write\$4)) same double adj5 buffer\$4 and (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable)).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 15:51
18	BRS	L18	456	((input\$4 and output\$4) or (read\$4 and write\$4)) same double adj5 buffer\$4 and (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable))	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 15:51
19	BRS	L19	19	((input\$4 and output\$4) or (read\$4 and write\$4)) same double adj5 buffer\$4 same (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable))	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 15:59
20	BRS	L20	34	(double adj5 buffer\$4 same (eeprom or non adj3 volatile or eprom or electrically ajd3 erasable))	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:00

	Type	L #	Hits	Search Text	DBs	Time Stamp
21	BRS	L21	108	(page or frame) with (double adj5 buffer\$4 or ping adj5 pong).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 16:21
22	BRS	L22	5	(page or frame) with (double adj5 buffer\$4 or ping adj5 pong).ab. and (eprom or eeprom or non adj3 volatile or electrical\$3 adj5 program\$5)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 16:27
23	BRS	L23	2	(page or frame) with (double adj5 buffer\$4 or ping adj5 pong) same (eprom or eeprom or non adj3 volatile or electrical\$3 adj5 program\$5)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 16:37
24	BRS	L24	3227	buffer\$4 same ((input\$4 and output\$4) or (read\$4 and writ\$4)) same (eprom or eeprom or non adj3 volatile or electric\$5 adj5 eraseable)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/0 2 16:41

	Type	L #	Hits	Search Text	DBs	Time Stamp
25	BRS	L25	1825	buffer\$4 with ((input\$4 and output\$4) or (read\$4 and writ\$4)) same (eeprom or eprom or non adj3 volatile or electric\$5 adj5 eraseable)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:41
26	BRS	L26	944	buffer\$4 with ((input\$4 and output\$4) or (read\$4 and writ\$4) or (load\$4 or unload\$4)) with (eeprom or eprom or non adj3 volatile or electric\$5 adj5 eraseable)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:42
27	BRS	L27	103	(buffer\$4 with ((input\$4 and output\$4) or (read\$4 and writ\$4) or (load\$4 or unload\$4)) with (eeprom or eprom or non adj3 volatile or electric\$5 adj5 eraseable)).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:44
28	BRS	L28	39	buffer\$4 with ((input\$4 and output\$4) or (read\$4 and writ\$4) or (load\$4 or unload\$4)) with (parallel or concurrent\$4 or simultaneous\$4 or "same" adj2 time) with (eeprom or eprom or non adj3 volatile or electric\$5 adj5 eraseable)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:46

	Type	L #	Hits	Search Text	DBs	Time Stamp
29	BRS	L29	75	buffer\$4 with ((input\$4 and output\$4) or (read\$4 and writ\$4) or (load\$4 or unload\$4)) with (parallel or concurrent\$4 or simultaneous\$4 or "same" adj2 time) same (eeprom or eprom or non adj3 volatile or electric\$5 adj5 eraseable)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:47
30	BRS	L30	137	((double adj5 buffer\$4) same (speed or increas\$4 or rate or faster)).ab.	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/11/02 16:56

PGPUB-DOCUMENT-NUMBER: 20040076046

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040076046 A1

**TITLE: Method of storing a quantity of data in a target
memory location and storage system**

PUBLICATION-DATE: April 22, 2004

US-CL-CURRENT: 365/200

APPL-NO: 10/ 653536

DATE FILED: September 2, 2003

RELATED-US-APPL-DATA:

child 10653536 A1 20030902

parent continuation-of PCT/EP01/14348 20011206 US UNKNOWN

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	DOC-ID	APPL-DATE
DE	101 10 153.8	2001DE-101 10 153.8	March 2, 2001

----- KWIC -----

Summary of Invention Paragraph - BSTX (8):

[0007] U.S. Pat. No. 4,922,456 relates to a method of reducing wearout in a non-volatile memory with double buffer. Before a write operation is performed, information to be stored is written to a double buffer in order to be able in case of a disturbance to reconstruct the steps to be performed during the write operation. A flag is set indicating that the double buffer is valid. The data from the double buffer are then written to the appropriate locations of a non-volatile memory. Finally, the flag is cleared so that the memory management system knows that it is no longer the data in the double buffer that are valid, but the data in the non-volatile memory.

US-PAT-NO: 5947255

DOCUMENT-IDENTIFIER: US 5947255 A

TITLE: Method of discriminating paper notes

DATE-ISSUED: September 7, 1999

US-CL-CURRENT: 194/207, 250/556 , 382/135

APPL-NO: 08/ 834210

DATE FILED: April 15, 1997

FOREIGN-APPL-PRIORITY-DATA:		
COUNTRY	APPL-NO	APPL-DATE
JP	8-115245	April 15, 1996

----- KWIC -----

Detailed Description Text - DETX (5):

In the image processing/judgment section 10, the video signal VSB is accumulated in a FIFO (First-In First-Out) memory 11 and the video signal also is sequentially transferred and written to a selected region of a main memory (double buffers) 12 via the correcting section 101 in a digital signal processor (DSP) 100. The DSP 100 cooperates with a ROM 110 in which control

programs are stored to develop the image data of the amount of a bill in the main memory 12. The DSP 100 has a blocking and compression encoding section 102 which blocks and compression-encodes the video signal VSB which is inputted via the FIFO memory 11, and also has a comparison/judgment control section 103 which outputs a judgment result DR. Also, the image processing/judgment section 10 has a flash memory 13 for reference-code patterns in which the reference-code patterns for various bills are stored. The reference-code pattern RC and the compressed and encoded data CS of a discriminated bill which is from a part of the main memory 12, are compared at the comparison/judgment control section 103, and the judgment result DR is outputted. The image processing/judgment section 10 performs data communication with a discriminator control section 20 which controls a discriminator (bill validator) through a dual port RAM 14. Note that the flash memory 13 is an electrically rewritable read-only memory and that the main memory 12 functions as double buffers and is a RAM having an image data memory, a work area memory, etc.

PAT-NO: JP409284515A

DOCUMENT-IDENTIFIER: JP 09284515 A

TITLE: IMAGE PROCESSING UNIT

PUBN-DATE: October 31, 1997

INVENTOR-INFORMATION:

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APPL-NO: JP08115283

APPL-DATE: April 15, 1996

INT-CL (IPC): H04N001/21, B41J005/30 , G03G015/00

ABSTRACT:

PROBLEM TO BE SOLVED: To obtain the image processing unit

that recovers
image data even when a print buffer memory is fully occupied by
compressing
image data stored in a page memory depending on a residual
amount of the print
buffer memory and using the page memory as a double buffer.

SOLUTION: A controller 123 compares residual amount detection
signals 198,
199 with a predict value of the occupied capacity of the PBM 15
obtained from a
compression rate prediction circuit 160 and when the controller
123
discriminates it that the predicted value causes no margin in the
residual
amount, magnification/resolution conversion circuits 125, 126
compress the
image data signal to 1/2 to store the compressed signal to page
memory circuits
119, 120 having a storage capacity by one page. Thus, the page
memory circuits
119, 120 act like a double buffer storing (n-1)th and n-th
compressed image
data and even when the PBM 15 reaches a fully occupied state
during reading of
the n-th data and the reading is stopped, the image data are
recovered.

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